ABSTRACT OF THE DISCLOSURE

One embodiment of this invention is an OFDM PLC transceiver ASIC built with one or more MCU's (optional), DSP's and hardwired logic blocks, based on this architecture, that would be used for in-home, MDU and access applications having three separate program loads. Once the program has completed loading, the transceiver would operate and meet all the requirements (technical and regulatory standards) for that application segment.

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